

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR PATTERNING A SEMICONDUCTOR REGION	
Application Number :		
Date :		
First Named Applicant:	Sadanand V. Deshpande	
Confirmation Number:		
Attorney Docket Number:	FIS920040061US1	
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Documents being submitted:	Files
us-fee-sheet	FIS920040061US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	FIS920040061US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	FIS920040061US1-usassn.xml us-assignment.xsl us-assignment.dtd 040061asgn1.tif 040061asgn2.tif
us-request	FIS920040061US1-usrequ.xml us-request.dtd us-request.xsl
application-body	fis920040061us1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd 20040061US1-dwg1.tif 20040061US1-dwg2.tif
us-declaration	040061decl1.tif
us-declaration	040061decl2.tif
us-declaration	040061decl3.tif
Comments	
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